

FIGURE 1: THREE-STAGE CLOS ARCHITECTURE

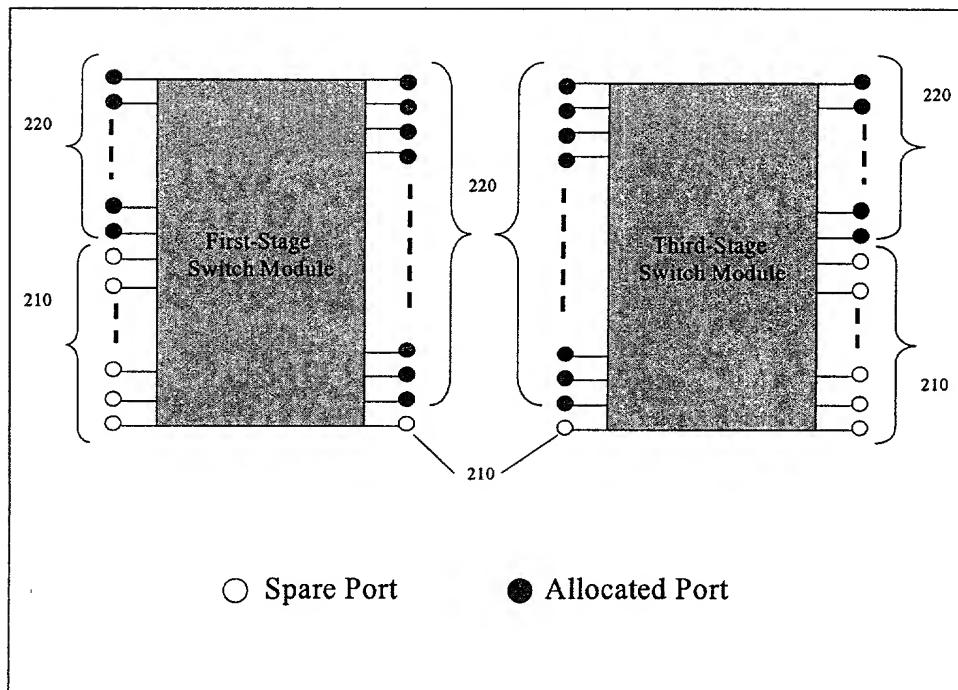


FIGURE 2: PORT ALLOCATIONS IN SWITCH MODULES

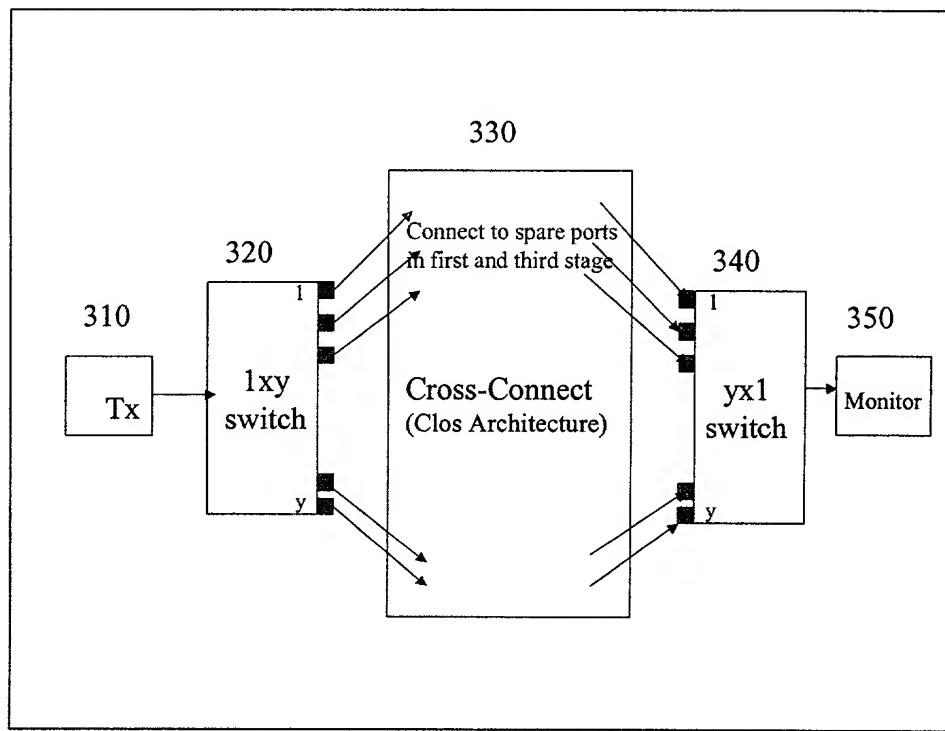


FIGURE 3: FAULT ISOLATION EQUIPMENT SETUP

FIGURE 4

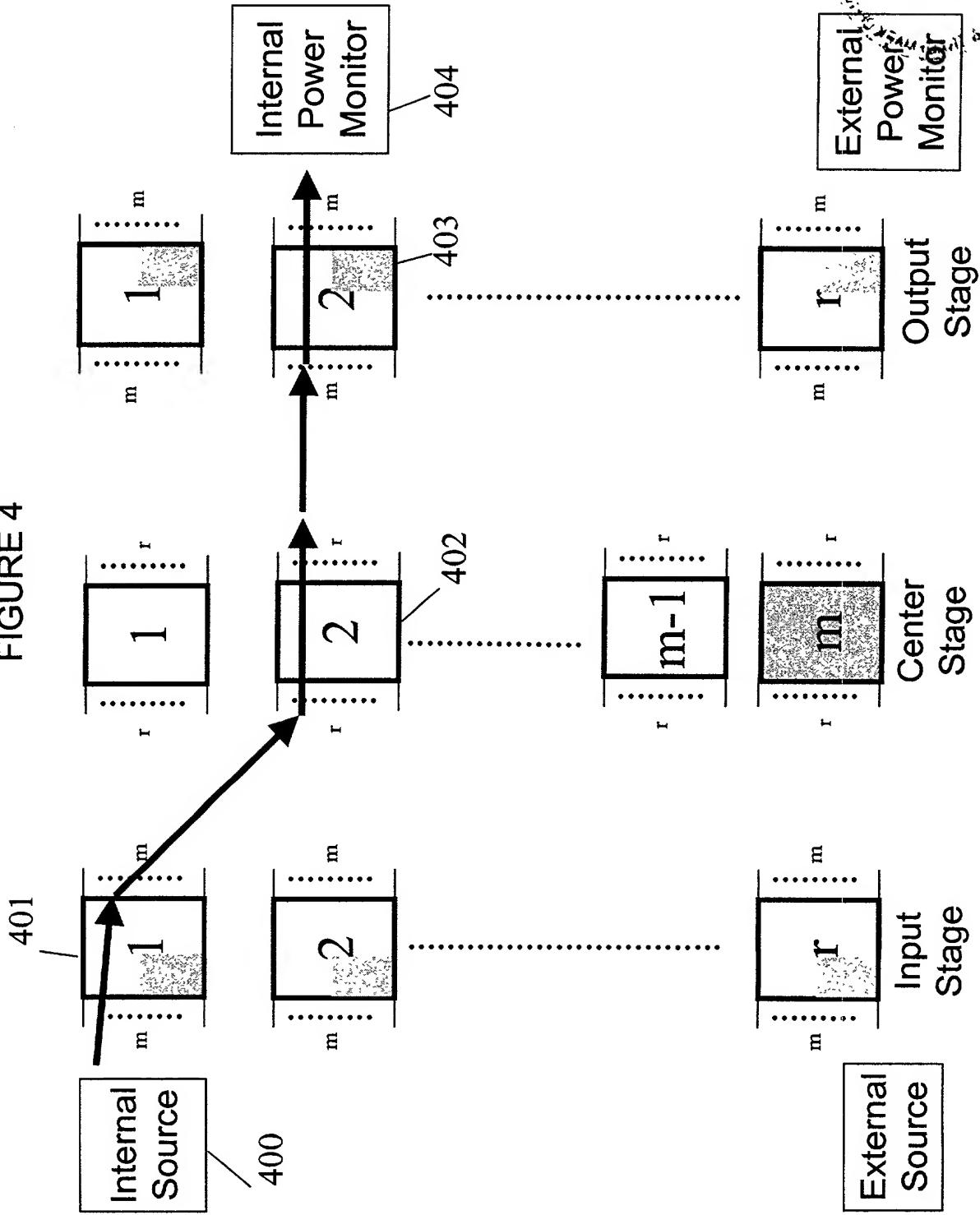


FIGURE 5

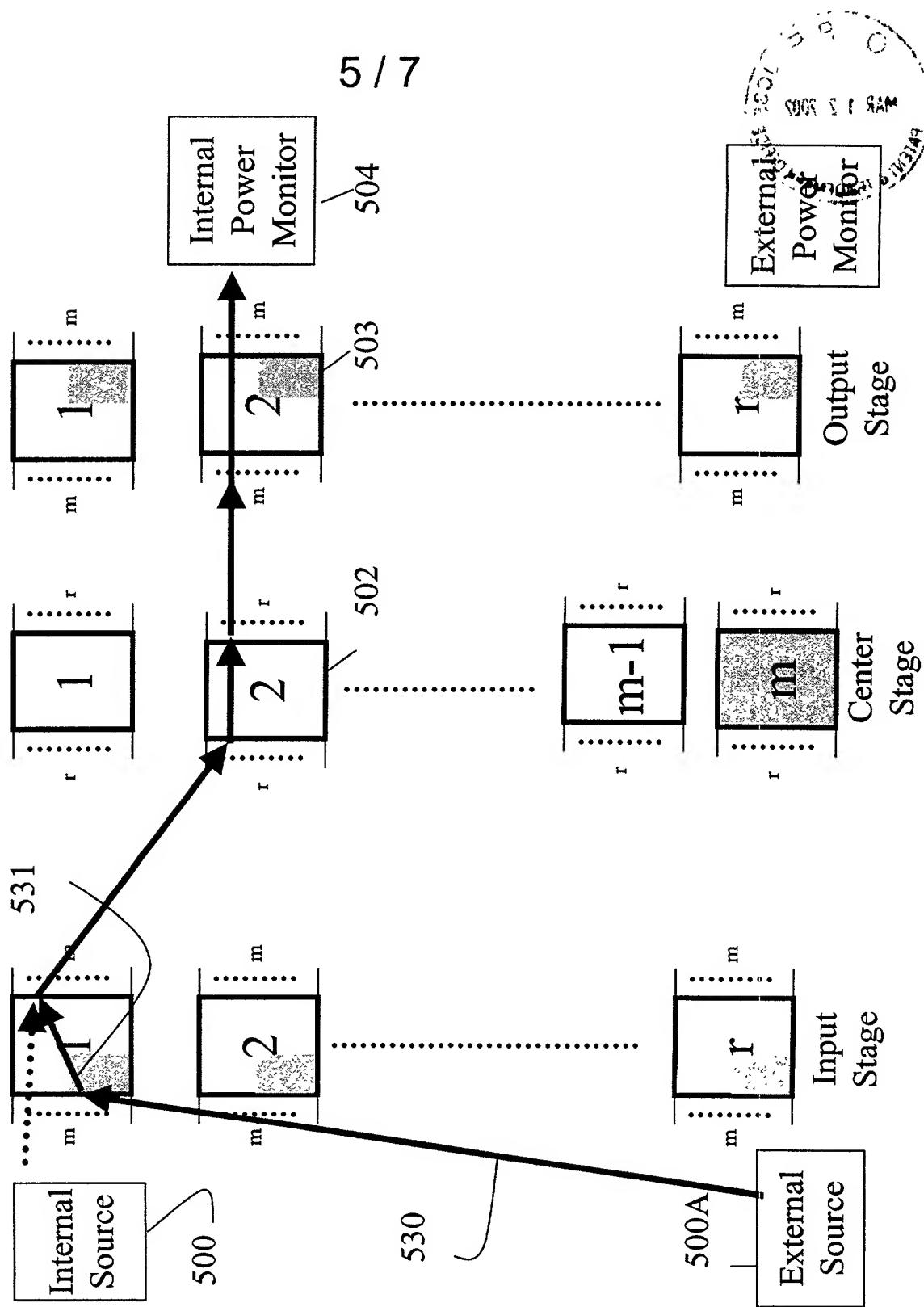


FIGURE 6

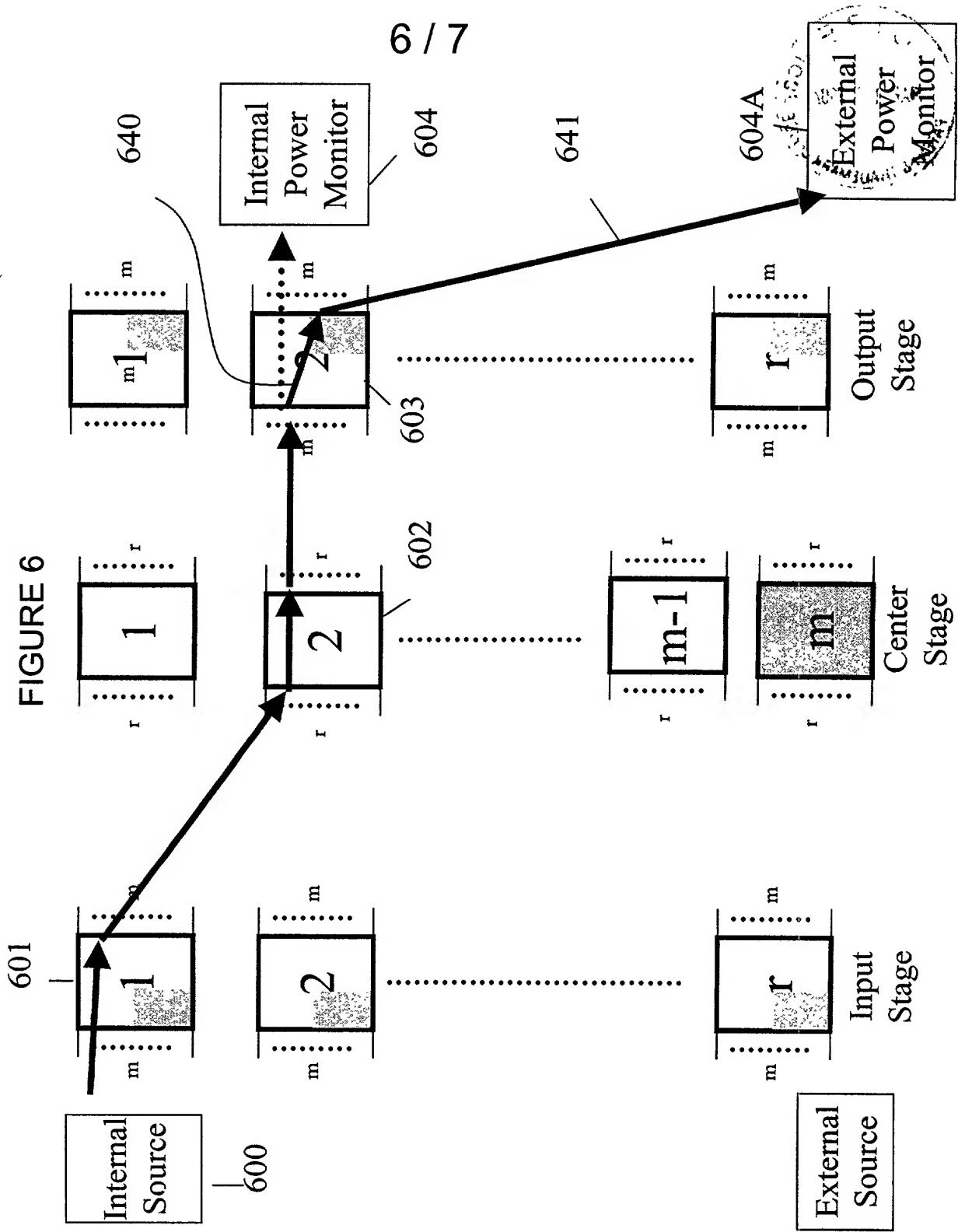


FIGURE 7

